

On-the-fly Stuttering in the Construction of Deterministic ω -Automata

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Context

Goal

- Deterministic ω -Automata (automata on infinite words) from LTL formulas

Problem

- LTL to nondeterministic Büchi automata:
Exponential blow-up
- Determinization of nondet. Büchi automata: $2^{\mathcal{O}(n \log n)}$

In practice

- Implementation *ltl2dstar*, several heuristics
- Used e.g. by *LiQuor*, LTL model checker for Markov Decision Processes

Overview

Idea

- Use knowledge about *insensitiveness to stuttering* to avoid intermediate automata states during construction of deterministic automaton

Algorithm

- On-the-fly
- Can be used for deterministic automata with Rabin, Streett, Parity, Büchi acceptance
- During determinization, but also union, etc.
- Heuristic

Outline

- 1 Introduction
 - LTL, ω -Automata
- 2 Stuttering deterministic ω -automata
 - Stuttering
 - Stuttered Construction
 - Partial Stuttering
 - Experimental Results
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Linear Temporal Logic (LTL)

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- over set of Atomic Propositions AP ($\Sigma = 2^{AP}$)
- Propositional Logic: $\neg, \vee, \wedge, \rightarrow, \dots$
- Temporal Operators:

U	Until	<i>try U success</i>
F	Finally, Eventually	<i>F success</i>
G	Globally, Always	<i>G \neg error</i>
X	Nextstep	<i>try \wedge X success</i>

Deterministic ω -Automaton

Deterministic ω -Automaton

$\mathcal{A} = (Q, \Sigma, \delta, q_0, \Omega)$, with

- Q : Set of states
- Σ : Alphabet
- δ : Transition function (deterministic, $Q \times \Sigma \rightarrow Q$)
- q_0 : Starting state
- Ω : Acceptance condition

Acceptance/Language

- Accepts/rejects infinite words $\alpha \in \Sigma^\omega$
- $\mathcal{L}(\mathcal{A}) \subseteq \Sigma^\omega$

Rabin acceptance (k acceptance pairs)

- $Acc = \{\text{○}, \text{●}, \text{●}\}$
- Every state has acceptance signature: k -tuple $acc \in Acc^k$
- Run π over α is accepting iff $\exists i$:
 - $acc[i] = \text{●}$ infinitely often and
 - $acc[i] = \text{●}$ not infinitely often

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 $acc[i] = \text{●}$ not infinitely often
- Ordering: $\text{○} < \text{●} < \text{●}$
- $\max\{\text{acceptance signatures}\}$: maximum element-wise
 $\max\{\text{○●}, \text{●●}\} = \text{●●}$
- Calculate $acc_{inf} = \max\{acc(\text{inf}(\pi))\}$
- Run π is accepting iff $\exists \text{●}$ in acc_{inf}

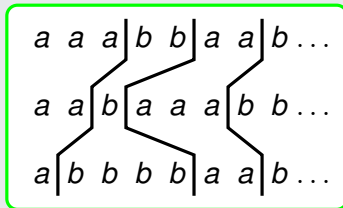
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Stuttering

Stutter Equivalence

Stutter equivalent words differ only by finite repetition (stuttering) of symbols.



Closure under stuttering

Language \mathcal{L} is closed under stuttering iff any two stutter equivalent words are both accepted or both rejected.

Stutter Insensitiveness

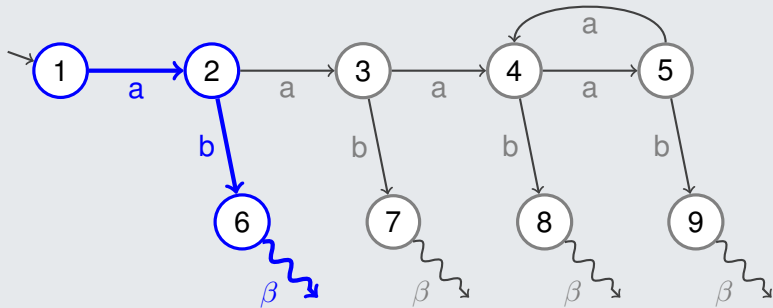
Stuttering Insensitiveness

- LTL formula, ω -automaton insensitive iff language is closed under stuttering
- Important property for e.g. partial-order reduction in model checking

How can we know?

- In general: PSPACE-complete in formula size
- All formulas without NextStep (X) are insensitive

Run in stutter insensitive DRA

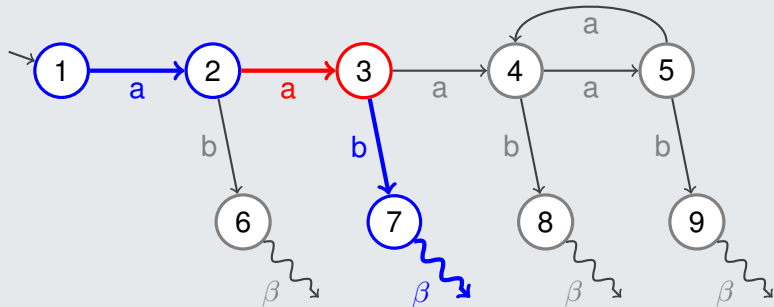


Input word $\alpha = ab\beta$

Automaton is stutter insensitive \Rightarrow

Stuttering does not change acceptance of α

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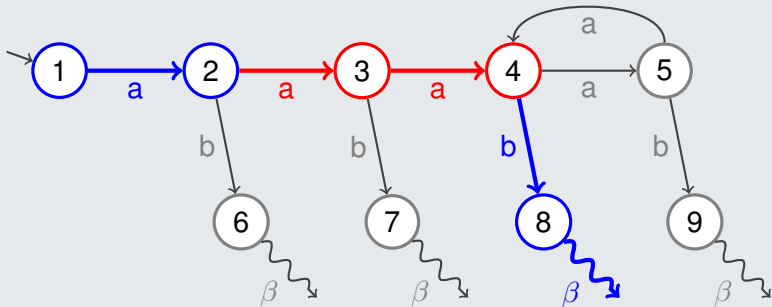


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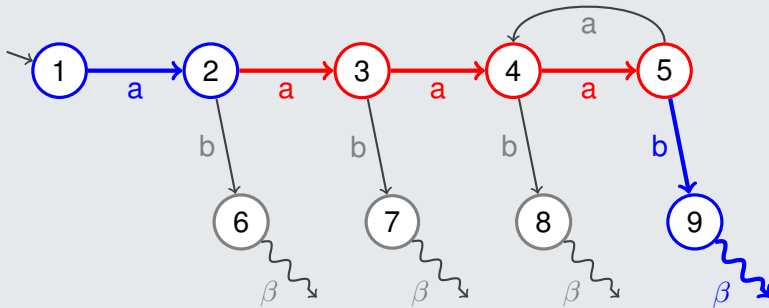


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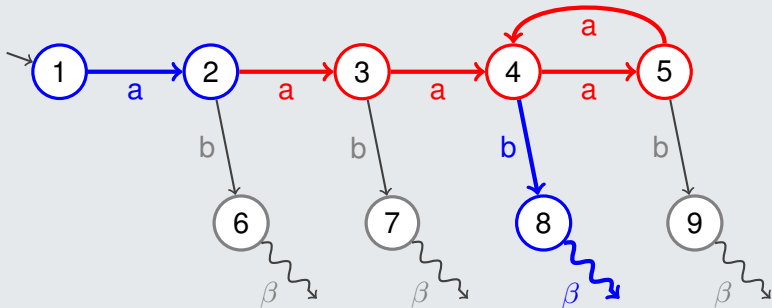


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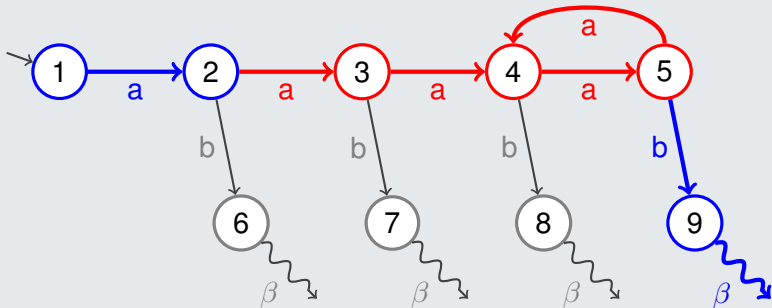


Input word $\alpha = \mathbf{a} \mathbf{a} \mathbf{a} \mathbf{a} \mathbf{a} \mathbf{b} \beta$

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Stuttered automaton

Stuttered Construction

Original DRA $\mathcal{A} = (Q, \Sigma, \delta, q_0, \Omega)$

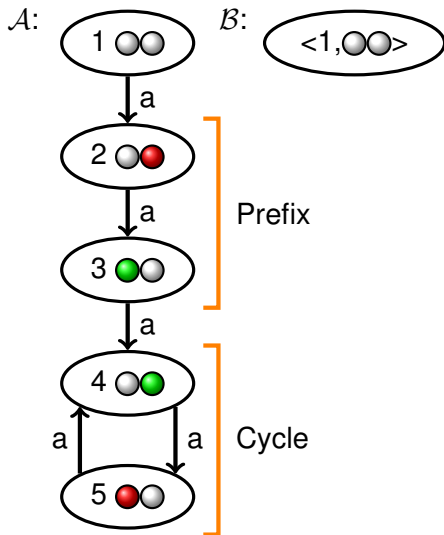
Stuttered DRA $\mathcal{B} = (Q', \Sigma, \delta', q'_0, \Omega')$

- $Q' = Q \times Acc^k$
- $q'_0 = \langle q_0, acc(q_0) \rangle$
- Ω' : according to the acceptance signature stored in the state

Stuttered transition function δ'

- $\delta'(\langle q, acc \rangle, a) = ?$

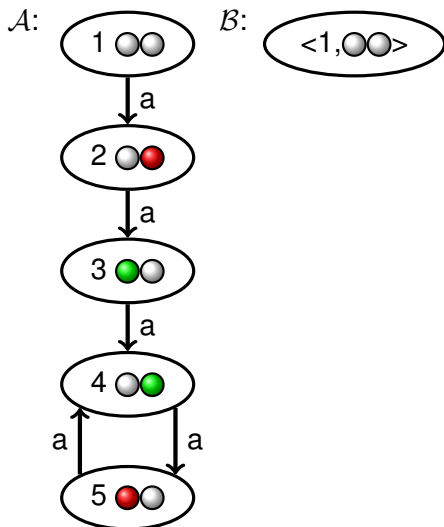
- $\delta(q, a^i)$ in \mathcal{A} , $i = 1, 2, \dots$
- Prefix and cycle



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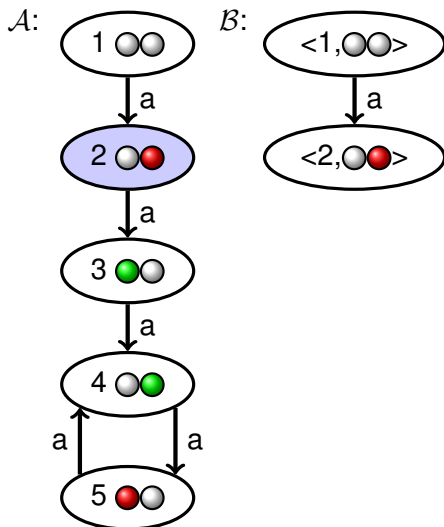
- $\delta(q, a^i)$ in \mathcal{A} , $i = 1, 2, \dots$
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- Merge acc. signatures with max operator



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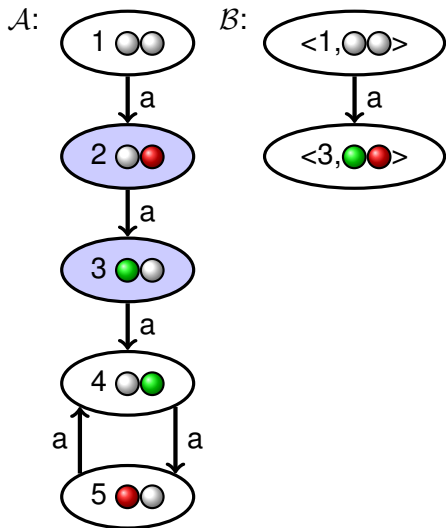
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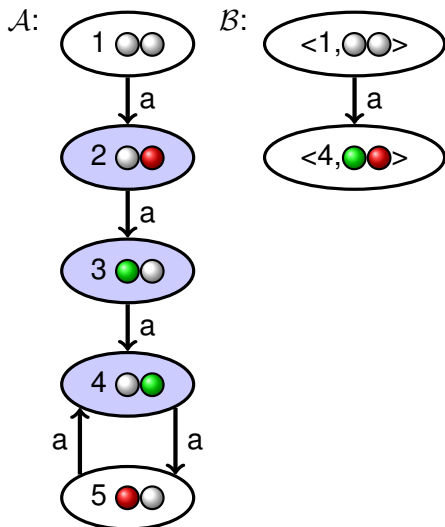
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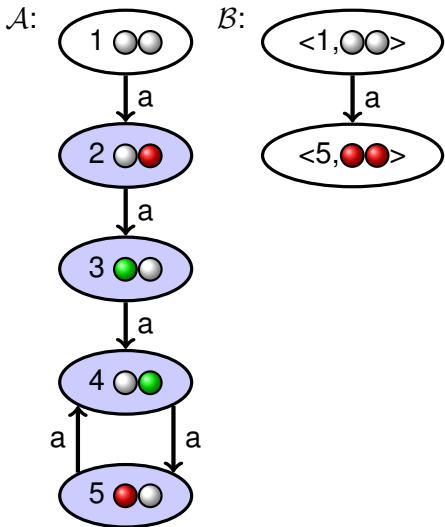
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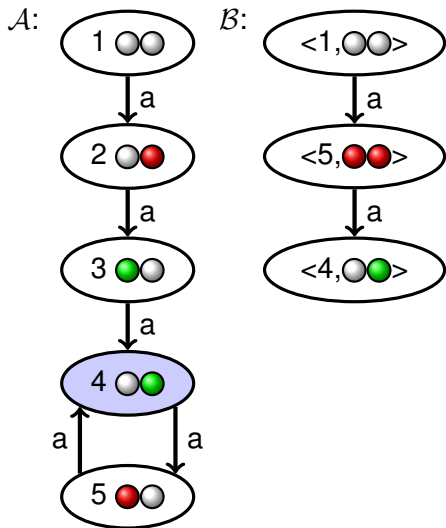
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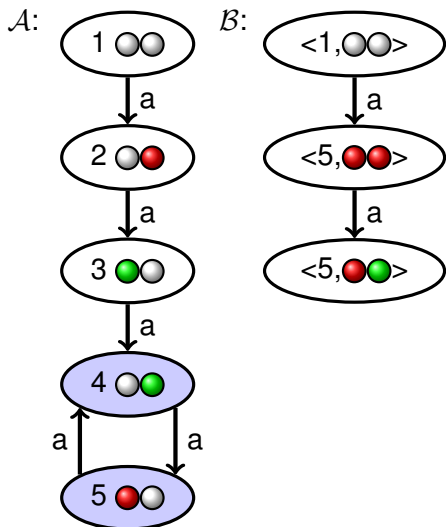
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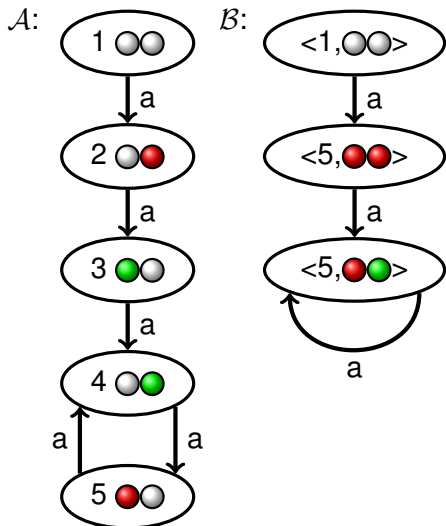
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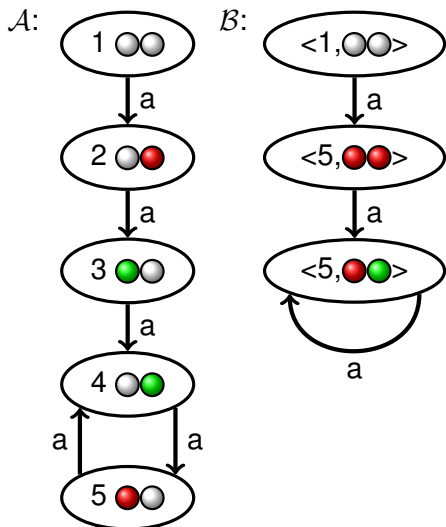
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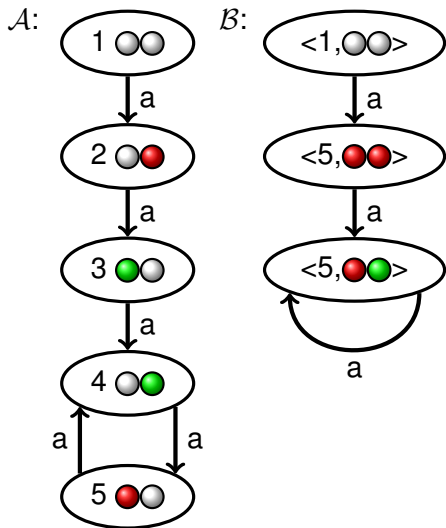
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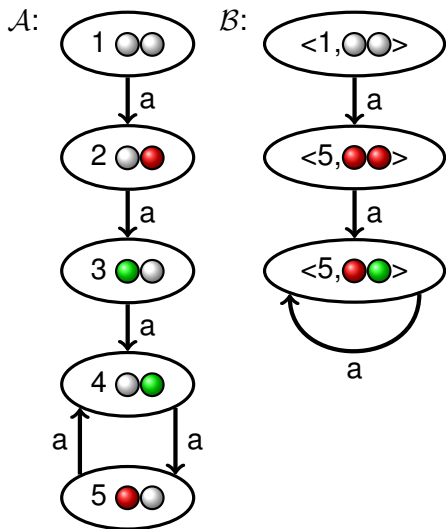
- If acc. sig. of cycle “dominates” the prefix acc. sig., prefix and cycle states collapse
- Prefix state can not always be avoided



Stuttered transition function δ'

- $\delta'(\langle q, acc \rangle, a) = ?$

- If acc. sig. of cycle “dominates” the prefix acc. sig., prefix and cycle states collapse
- Prefix state can not always be avoided
- \mathcal{B} can be larger than \mathcal{A}



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Using Partial Stuttering

Partial Stuttering

- Only stuttering of certain symbols from Σ is allowed.
- For formula, determine stutter insensitive symbols $S \subseteq \Sigma$
- Use stutter construction only on transitions with $\sigma \in S$

Determining Stutter Insensitiveness

- Prototypical implementation
- Checking each $\sigma \in \Sigma$
- NBA emptiness check of product automaton of stutter closure NBA and complement formula NBA

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Benchmark

Formulas

Literature 39 Formulas (25 without X)

- Etessami, Holzmann:
Optimizing Büchi Automata
- Somenzi, Bloem:
Efficient Büchi automata from LTL formulae

Pattern 55 Formulas (30 without X)

<http://patterns.projects.cis.ksu.edu/>

Random 1000 Random Formulas (415 without X)
Generated with the Test-Bench `lbtt`

- LTL to NBA translator: `ltl2ba`
- Generating det. Rabin automata (Safra's algorithm)

Results (without X)

Formulas	DRA	stuttered DRA	Time
25 Literature	278	168 (-40%)	0.3s / 0.3s
30 Patterns	311	189 (-39%)	0.3s / 0.3s
415 Random	1,820	1,499 (-18%)	3.9s / 4.0s

Sum of the number of states of the automata, running time

Results (with partial stuttering)

Formulas	DRA	stuttered DRA	Time
14 Literature	107	79 (-26%)	0.3s / 6.8s
25 Patterns	103,318	17,731 (-83%)	207.5s / 99.4s
585 Random	3,441	3,081 (-11%)	5.6s / 10.6s

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Sum of the number of states of the automata, running time

Formulas	Calc. S	Calc. DRA	Avg. $ S / \Sigma $
14 Literature	6.5s	0.3s	75.9%
25 Patterns	15.5s	83.9s	92.7%
585 Random	3.8s	6.8s	49.6%

Summary

- Use stutter insensitiveness to generate smaller deterministic ω -automata in practice
- Partial stuttering where not all symbols are stutter insensitive
- Calculating set S for formulas with X operator is expensive but feasible in practice

<http://www.ltl2dstar.de/>